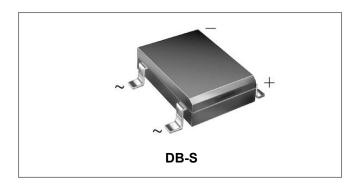






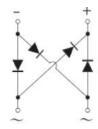
# DB101S THRU DB107S SINGLE-PHASE GLASS PASSIVATED SILICON BRIDGE RECTIFIERS



#### **Features**

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-0
- This is a Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

## **Circuit Diagram**



# Mechanical Data

- Case: DB-S, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Lead Free: For RoHS / Lead Free Version,

#### Maximum Ratings@TA=25°C unless otherwise specified

Single Phase half wave 60Hz, resistive or inductive load. For capacitive load current derate by 20%.

Characteristic	Symbol	DB 101S	DB 102S	DB 103S	DB 104S	DB 105S	DB 106S	DB 107S	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Average Forward Output Current (Note 1) @ T <sub>C</sub> =100°C	I <sub>F(AV)</sub>	1.0					Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	45					A		
I <sup>2</sup> t Rating for Fusing (t < 8.3ms)	l²t	8.404					A <sup>2</sup> s		

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### **Electrical Characteristics:**

Characteristic	Symbol	DB 101S	DB 102S	DB 103S	DB 104S	DB 105S	DB 106S	DB 107S	Unit
Maximum Forward Voltage Drop per Bridge Element @I <sub>F</sub> =1.0A, T <sub>J</sub> =25°C	V <sub>F</sub>				1.0				V
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 125°C	I <sub>R</sub>				5 200				μΑ
Typical Junction Capacitance (Note 2)	С				25				pF

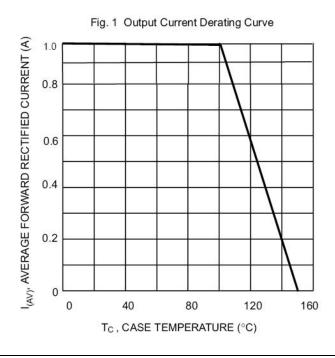
<sup>\*</sup> Pulse width < 300  $\mu$ s, duty cycle < 2%

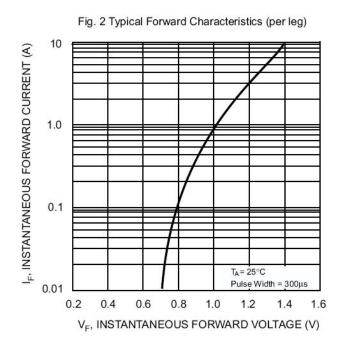
## **Thermal-Mechanical Specifications:**

Characteristic	Symbol	DB 101S	DB 102S	DB 103S	DB 104S	DB 105S	DB 106S	DB 107S	Unit
Typical Thermal Resistance Junction to Ambient	R <sub>0JA</sub>				40				°C/W
Typical Thermal Resistance Junction to Lead	R <sub>0JL</sub>						°C/W		
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55+150				°C			

Note: 1. Mounted on glass epoxy PC board with 1.3mm<sup>2</sup> solder pad.

## **Ratings and Characteristics Curves**





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<sup>2.</sup> Measured at 1.0 MHZ and applied reverse voltage of 4.0 VDC





Fig. 3 Maximum Peak Forward Surge Current (per leg)

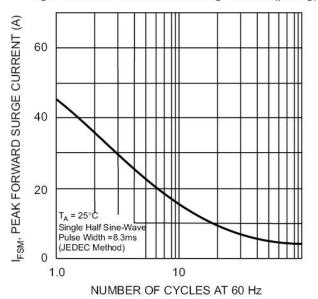


Fig. 4 Typical Reverse Characteristics (per element)

100

T<sub>J</sub>= 125°C

10

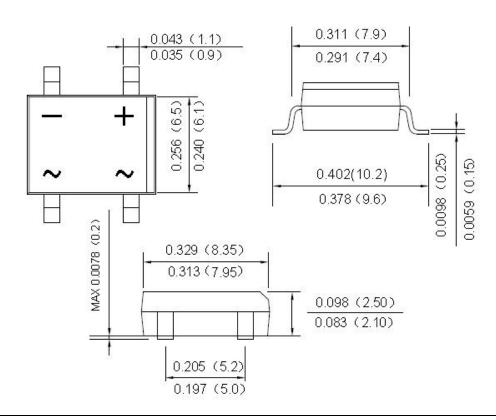
T<sub>J</sub>= 25°C

0.01

0 20 40 60 80 100 120 140

PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

## **Mechanical Dimensions DB-S(Inches/Millimeters)**



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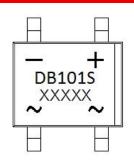


# Ordering Information

Device	Package	Shipping
DB101S THRU DB107S	DB-S (Pb-Free)	1500pcs / reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

## **Marking Diagram**



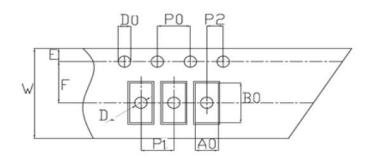
Where XXXXX is YYWWL

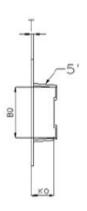
DB101S = Type Number
YY = Year
WW = Week
L = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

# **Carrier Tape Specification DB-S**





	I							
SYMBOL	Millimeters							
	Min.	Max.						
A0	8.65	8.95						
В0	10.31	10.51						
D0	1.50	1.60						
D1	1.40	1.60						
P0	3.90	4.10						
P1	11.90	12.10						
P2	1.90	2.10						
E	1.65	1.85						
K0	3.21	3.41						
F	7.40	7.60						
W	15.70	16.30						
Т	0.30	0.40						
10P0	39.80	40.20						

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## DB101S THRU DB107S

#### Technical Data Data Sheet N1459, Rev. B





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